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Coupled data communication provides a way forward, and this book, *Coupled Data Communication Techniques for High-Performance and Low-Power Computing*, gives a comprehensive overview for such coupled data techniques. Coupled data communication allows chips to communicate capacitively or inductively over short distances between chips without solder, and fundamentally shifts the design. [Coupled Data Communication Techniques for High-Performance ...](#)

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[Coupled Data Communication Techniques for High-Performance and Low-Power Computing \(Integrated Circuits and Systems\) \[Ron Ho, Robert Drost\] on Amazon.com. "FREE" shipping on qualifying offers.](#) Wafer-scale integration has long been the dream of system designers. Instead of chopping a wafer into a few hundred or a few thousand chips.

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[Coupled Data Communication Techniques for High-Performance ...](#)

This book provides an overview of the circuits, architectures, and chip packaging for coupled data techniques. It discusses the current research in chip-to-board capacitive coupling, chip-to-chip capacitive coupling, chip-to-chip inductive coupling, and chip-to-chip optical coupling.

Coupled Data Communication Techniques for High-Performance ...

Designers of next-generation high-performance computer systems face a host of technical challenges. For the past several decades, rising clock frequencies and increased chip integration have